

09/674,924

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,744,120 B1  
DATED : June 1, 2004  
INVENTOR(S) : Masahiko Yanagisawa

Page 1 of 1

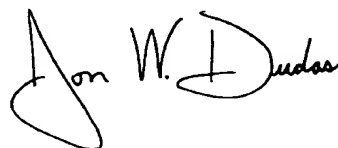
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page, Item [54] and Column 1, lines 1-4,

Title, "FLEXIBLE INTERCONNECT SUBSTRATE OF A TAPE-SHAPED SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE AND CIRCUIT BOARD" should read -- A FLEXIBLE INTERCONNECT SUBSTRATE OF A TAPE-SHAPED SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE AND CIRCUIT BOARD --

Signed and Sealed this

Seventeenth Day of May, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, sweeping initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*